

10/755,732

IFW



**PATENT**  
**30205/38071A**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application of: Jae Jin Lee

Serial No.: To be assigned

Filed: January 9, 2004 (Herewith)

For: Signal Delay Control Circuit in a  
Semiconductor Memory Device

Group Art Unit: To be assigned

Examiner: To be assigned

) I hereby certify that the documents  
) referred to as enclosed therewith are being  
) deposited with the United States Postal  
) Service on as first class mail, in an  
) envelope addressed to: Commissioner for  
) Patents, P.O. Box 1450  
) Alexandria, Virginia 22313-1450, on  
) **August 10, 2004,**

  
\_\_\_\_\_  
Michael R. Hull

**AMENDMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Dear Commissioner:

Please enter the following amendment in the above-referenced continuation application in response to the office action mailed on May 27, 2004.

Amendments to the claims and a complete listing of the claims begins on page 2 of this paper.

Remarks in support of the allowability of this application begin on page 7 of this paper.